

NOESIS TECHNOLOGIES, L.P.

www.noesis-tech.com



Noesis Technologies - Who is Who ^[1]

- Noesis Technologies is a provider of production proven, process independent and parameterizable telecom IP cores.
- Our mission is to provide a complete portfolio of high complexity telecom IP cores that will enable our customers to meet their design requirements in terms of function and performance, their expectations in terms of quality and service, as well as their time-to-market goals.
- Established at 2008 and hosted at Patras Science Park.

Noesis Technologies - Who is Who [2]

- Our IP cores present an industry-leading combination of high performance, low power and low die-area, as well as easy customization for adaptability to a wide range of applications in the System On a Chip (SoC) era.
- Our IP cores have been successfully integrated in a wide spectrum of projects ranging from telecom, industrial control and automotive applications to defense and aerospace systems.

Clients



Products

- Forward Error Correction
- Source coding
- Networking
- Security
- Telecom DSP

Internship Position

- Only MSc or last year BSc students are admissible
- The intern will be responsible for FPGA implementation of telecom algorithms
- Skills required:
 - ▶ Course credits in VLSI Design and embedded systems.
 - ▶ Experience in C/C++ and Matlab programming.
 - ▶ Experience in digital system design using VHDL or Verilog.
 - ▶ Familiarity with FPGA technology and embedded processors.

Contact Us

Noesis Technologies
Patras Science Park
GR-26504 Platani Rion
Patras, GREECE

Url : www.noesis-tech.com

Tel : +30-2610-911531

Email : info@noesis-tech.com